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represented by boxes A and B, are  
connected as shown in Fig.1. The  
reference direction for the current  $i$  in the  
interconnection and the reference polarity  
for the voltage  $v$  across the

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Reviews. 17. 1. Problem P8.3-3. The circuit shown in Figure P 8.3-3 is at steady state before the switch closes at time  $t = 0$ . Determine the capacitor voltage  $v(t)$  for  $t > 0$ . Step-by-Step Solution: Step 1 of 3.

The circuit shown in Figure P 8.3-3 is at



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voltage reference direction should be + on  
the right in part B: Page 28, caption for  
Figure 2.3-1: "current" instead of  
"cuurent" Page 41, line 2: "voltage or

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current" instead of "voltage or circuit" Page  
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